FIG.1

START

DELAY TIME
OF PACKET IN BUFFER
EXCEEDED PERMISSIBLE
VALUE?

YES

1502

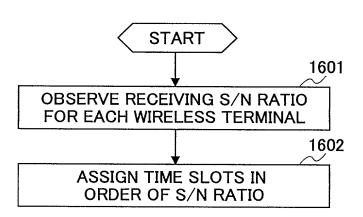
ASSIGN TIME SLOT IN ORDER OF
DELAY TIME OF PACKET IN BUFFER

1503

ASSIGN TIME SLOT IN ACSENDING ORDER
OF DATA AMOUNT OF PACKET IN BUFFER

OBLON, SPIVAK, ET AL DOCKET #: 214342US2 INV: Daisuke KITAZAWA, et al. SHEET <u>2</u> OF <u>17</u>

FIG.2





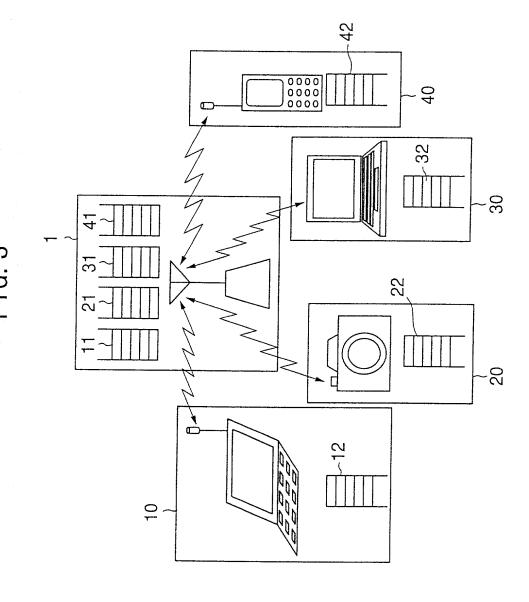


FIG.4

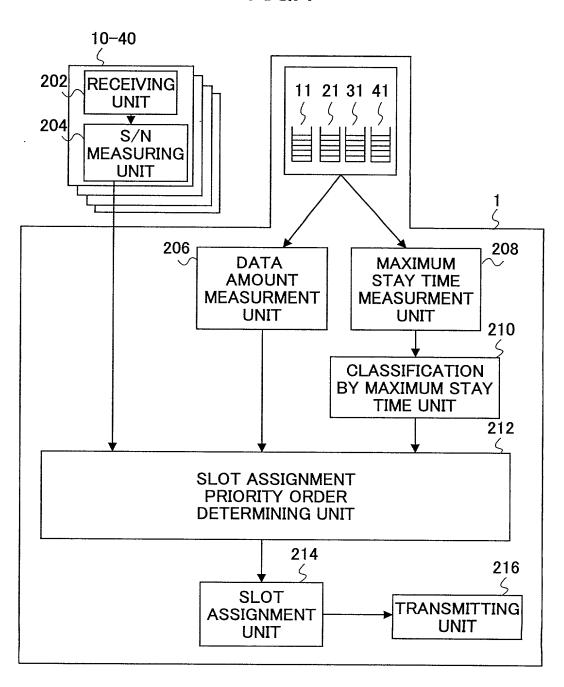


FIG.5

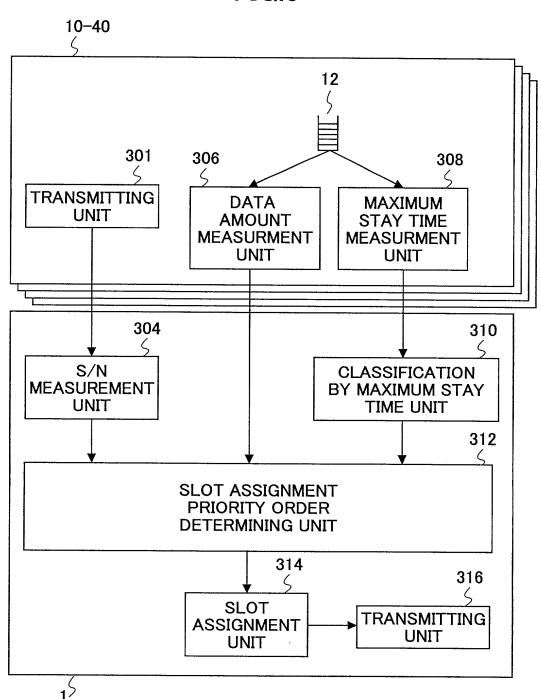


FIG.6 **START** 401 MAX REPEAT FOR NO **1ST PHASE** STAY TIME EXCEEDS NUMBER OF **PERMISSIBLE WIRELESS** TIME? **TERMINALS** 403 402 YES **NOT TIME** TIME EXCEEDING **EXCEEDING TERMINAL TERMINAL** 404 PROCESSING FOR TIME EXCEEDING **TERMINAL** 405 **2ND PHASE** NO **ANY VACANT \$LOT?** YES 406 PROCESSING FOR NOT TIME EXCEEDING TERMINAL **END**

FIG.7

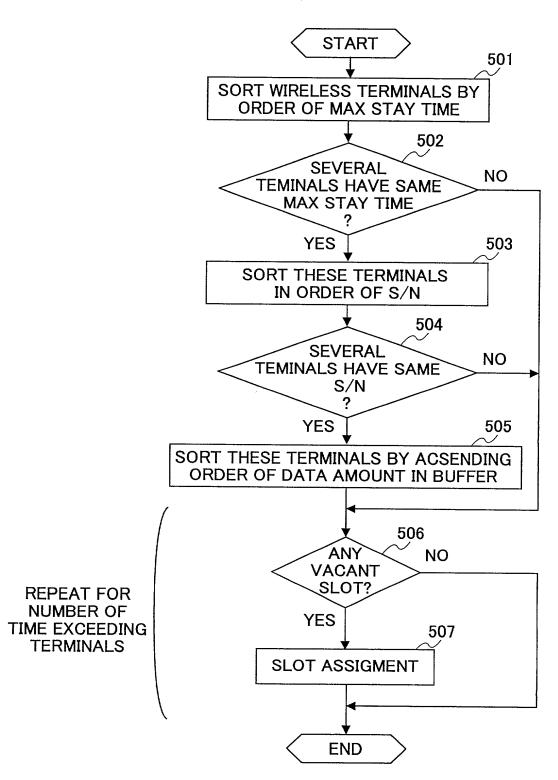


FIG.8

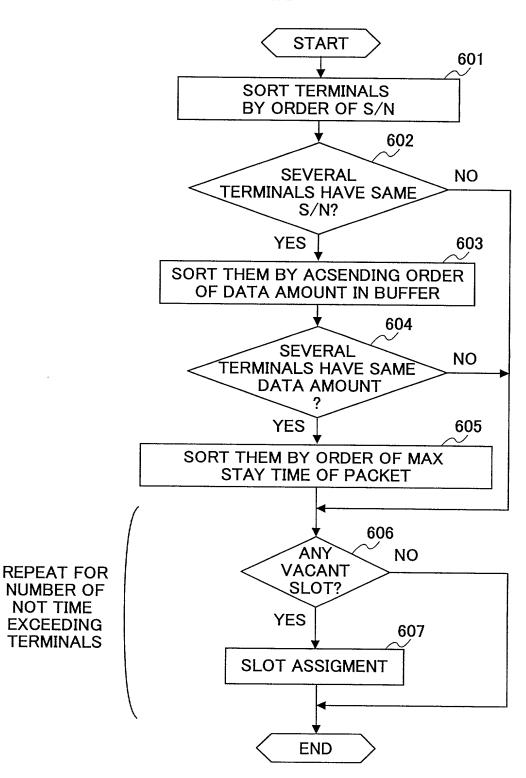


FIG.9

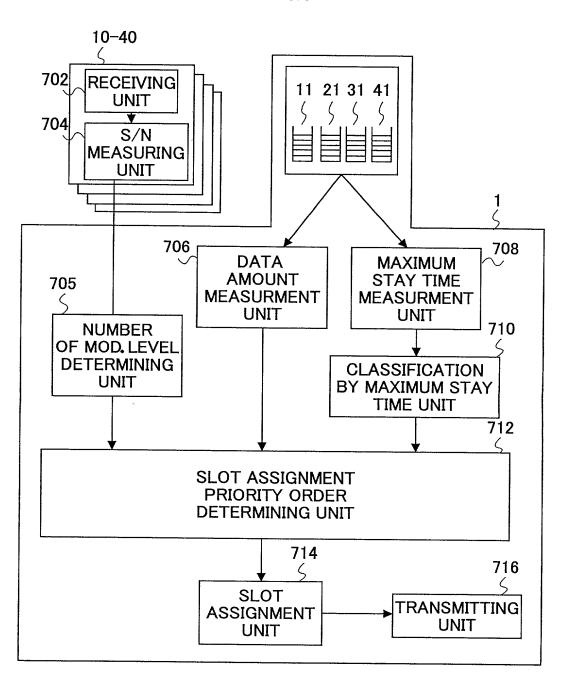


FIG.10

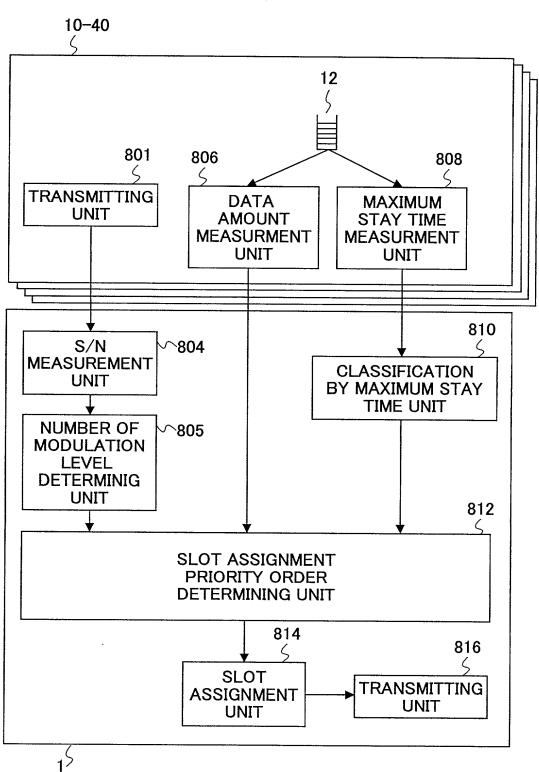


FIG.11

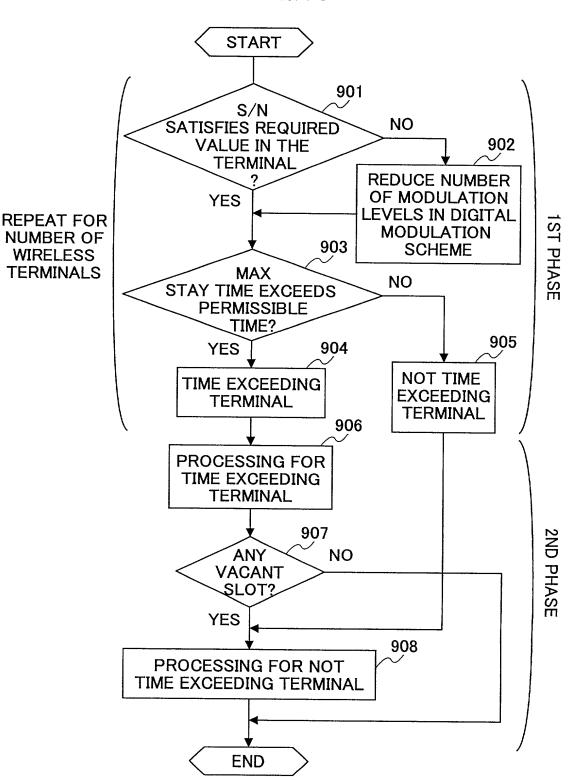


FIG 12

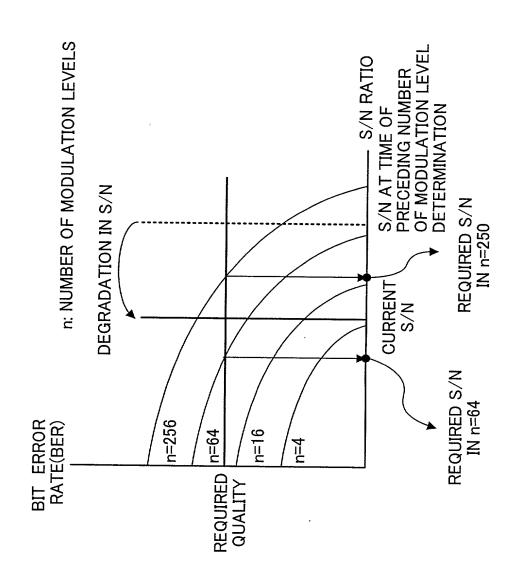


FIG.13

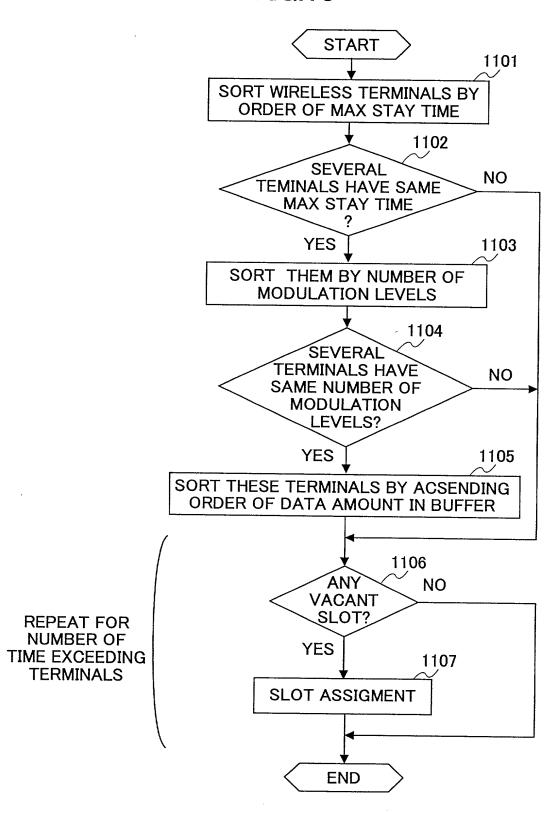
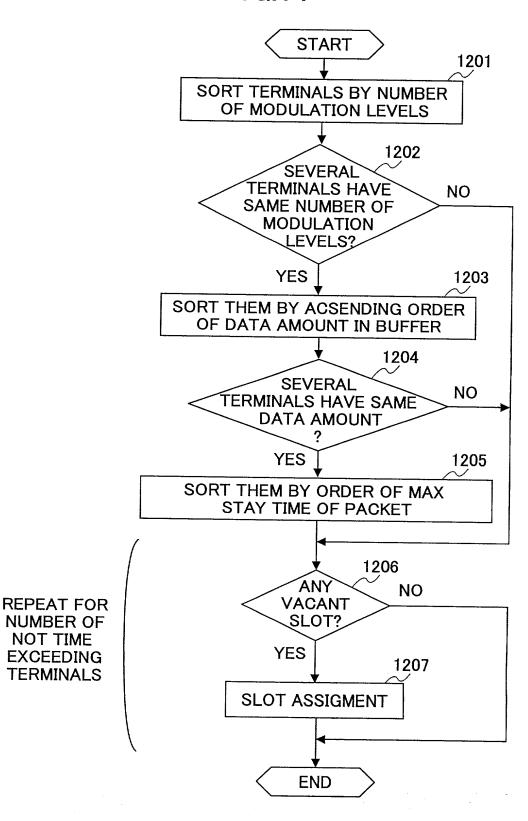
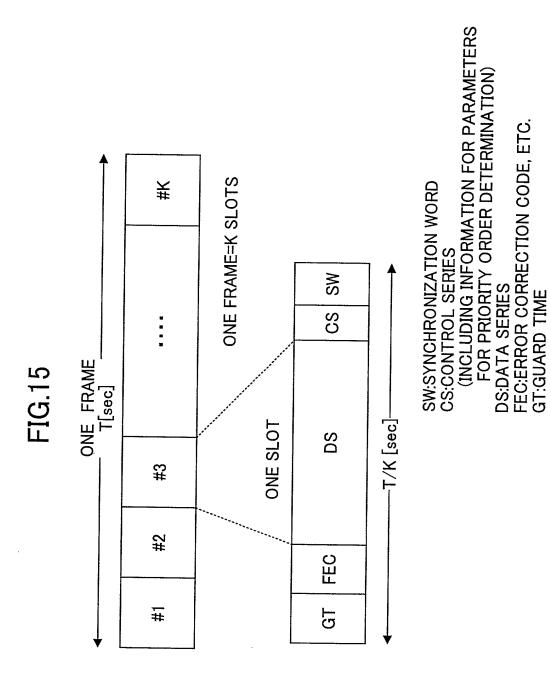


FIG.14





الية إ

1

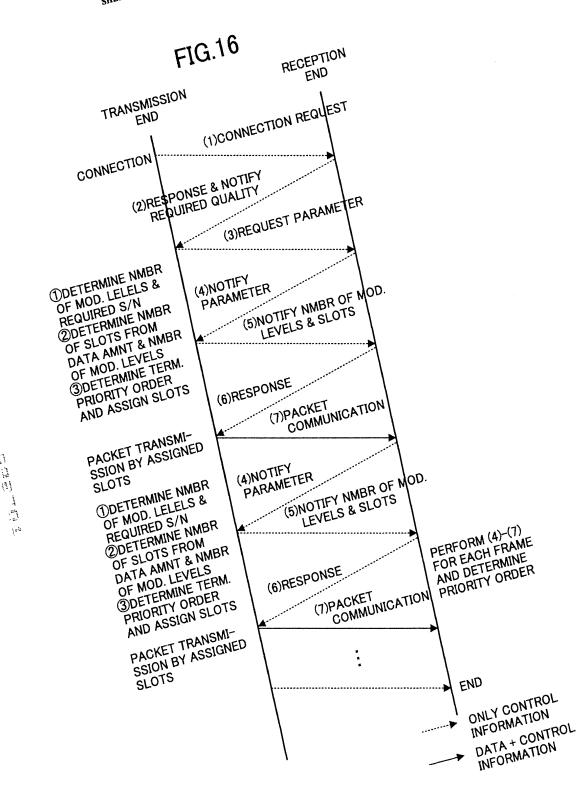


FIG.17

